3A 30V 650kHz Synchronous Step-Down Converter

FEATURES

- 3A Continuous Output Current
- Wide 4.5V to 30V Operating Input Range
- Output Adjustable from 0.8V to 5V
- Up to 93% efficiency
- Low Rds(on) Internal Switch
- Adaptive On Time Control
- Fast Transient Response
- Pseudo 650kHz Switching Frequency
- Programmable Soft Start
- Thermal Shutdown
- Cycle by Cycle Over Current Protection
- Short Circuit Protection
- Thermal Shutdown
- Available in SOP-8 Exposed Pad Package

APPLICATIONS

- Digital TV Power Supply
- High Definition Blu-ray Players
- Networking Home Terminal
- Set Top Box
- Multi-Function Printer

DESCRIPTION

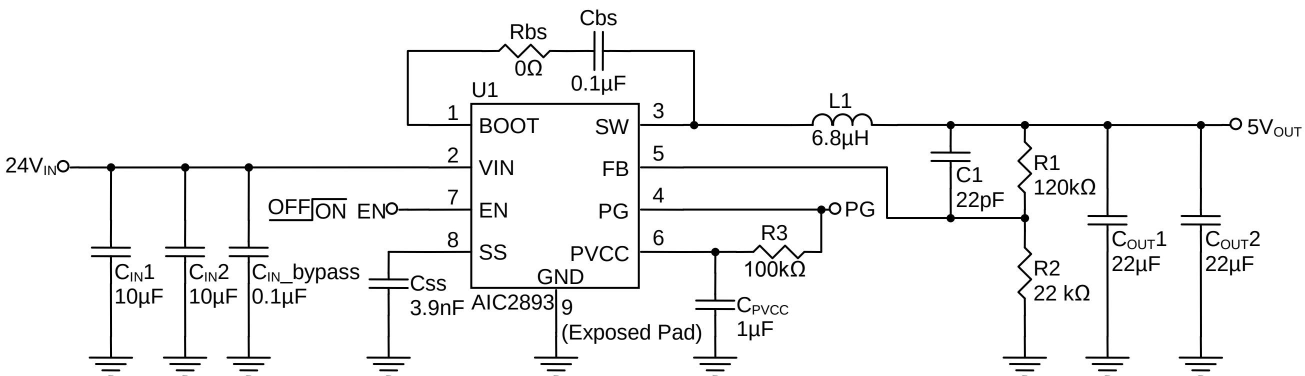
The AIC2893 is an adaptive on time control synchronous step down converter. The AIC2893 enables system designers to complete various power bus regulators with a cost effective, low component count, low standby current.

The AIC2893 main control loop uses adaptive on time control that provides a fast transient response without external compensation component. The low impedance internal MOSFETs support high efficiency operation with wide input voltage range form 4.5V to 30V.

The AIC2893 has a proprietary circuit that the device adapts to all ceramic capacitors. The output voltage can be adjustable between 0.8V to 5V. The device also features a programmable soft start time.

This device is available in SOP-8 exposed pad package and provides a very compact system solution with minimal external components and PCB area.

APPLICATIONS CIRCUIT



Typical Application Circuit

ORDERING INFORMATION

AIC2893XXXXXX

PACKING TYPE
TR: TAPE & REEL
TB: TUBE

PACKAGING TYPE
S8E: SOP-8 Exposed Pad

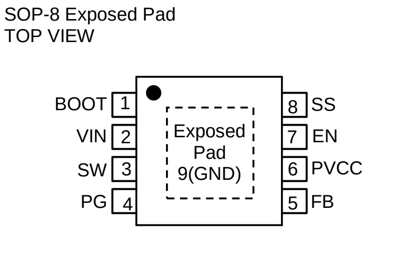
G: GREEN PACKAGE

Example:

AIC2893GS8ETR

→ Green SOP-8 Exposed Pad Package and TAPE & REEL Packing Type

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINS

Supply Voltage, V_{IN} …………………………………… -0.3V to 33V

Switch Voltage, SW …………………………………… -0.3V to V_{IN}+0.3V

BOOT to SW …………………………………… -0.3V to 6V

EN to GND …………………………………… -0.3V to 33V

FB, PVCC, SS, PG to GND …………………………………… -0.3V to 6V

Junction Temperature …………………………………… 150°C

Lead Temperature …………………………………… 260°C

Storage Temperature Range …………………………………… -65°C to 150°C

Operating Ambient Temperature Range …………………………………… -40°C to 85°C

Thermal Resistance Junction to Case SOP-8 Exposed Pad* …………………… 15°C/W

Thermal Resistance Junction to Ambient SOP-8 Exposed Pad* …………………… 60°C/W

(Assume no Ambient Airflow)

Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

*The package is place on a two layers PCB with 2 ounces copper and 2 square inch, connected by 8 vias.

ELECTRICAL CHARACTERISTICS

(V_{IN}=24V, V_{EN} =5V, T_A=25°C, unless otherwise specified.) (Note1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Operating Voltage	V_{iN}		4.5		30	V
Supply Current						
Shutdown Current	I_{SHDN}	V_{EN}=0V		0.1	1	μA
Quiescent Current	I_Q	V_{EN}=3V,V_{FB}=1V		0.55		mA
Logic Threshold						
EN High Level Threshold Voltage		V_{EN} Rising	1.64	1.79	1.94	V
EN Hysteresis				210		mV
V_{REF} Voltage and Feedback Input Current						
Feedback Reference Voltage	V_{REF}	4.5V≦V_{IN}≦30V	0.75	0.765	0.78	V
Feedback Input Current	I_{FB}	V_{FB}=0.8V	-0.15	0	0.15	μA
V_{PVCC} Output						
V_{PVCC} Output Voltage	V_{PVCC}	6V≦V_{IN}≦30V, 0≦I_{PVCC}≦5mA	5.35	5.65	5.85	V
Line Regulation		6V≦V_{IN}≦30V, I_{PVCC}=5mA			25	mV
Load Regulation		0≦I_{PVCC}≦5mA			100	mV
Output Current	I_{PVCC}	V_{IN}=6V,V_{PVCC}=4V		60		mA
Switch On Resistance						
High Side Switch On Resistance	$R_{DS(ON)H}$			110		mΩ
Low Side Switch On Resistance	$R_{DS(ON)L}$			60		mΩ
Current Limit						
Current Limit	I_{LIMIT}		4.2	5.5		A
Thermal Shutdown						
Thermal Threshold	T_{SD}			150		°C
Thermal Shutdown Hysteresis	ΔT_{SD}			30		°C
On-Time Timer Control						
On-Time	t_{ON}	V_{IN}=24V, V_{OUT}=3.3V		210		ns
Minimum Off-Time	$t_{OFF(MIN)}$			300		ns
Soft Start						
SS Charge Current		V_{SS}=0V, V_{FB}=0.4V		5.5		μA
SS Discharge Current		V_{SS}=0.5V		1		mA
Power Good						
PGOOD Threshold		V_{FB} Rising		95		%
		V_{FB} Falling		87		
PGOOD Sink Current		PGOOD=0.5V		3		mA
Under Voltage Lockout Voltage						
UVLO Threshold		V_{IN} Rising to Wake up V_{PVCC}	3.7	4.0	4.3	V
UVLO Hysteresis				300		mV

Note 1: Specifications are production tested at T_A=25°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with Statistical Quality Controls (SQC).

TYPICAL PERFORMANCE CHARACTERISTICS

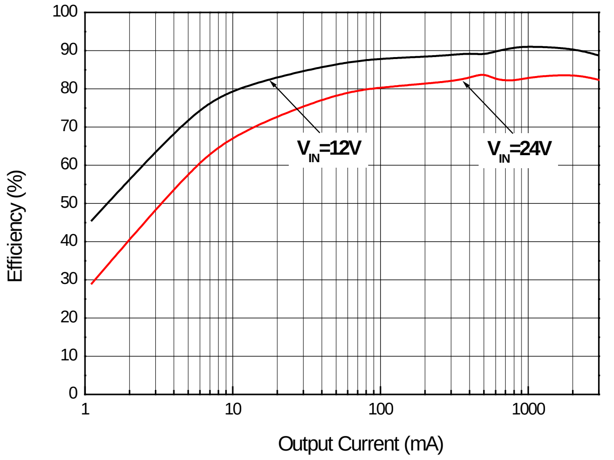


Fig. 1 Efficiency vs. Output Current (V_{OUT}=5V)

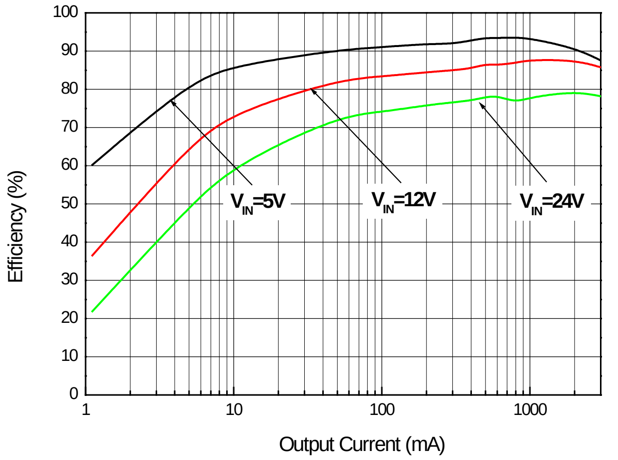


Fig. 2 Efficiency vs. Output Current (V_{OUT}=3.3V)

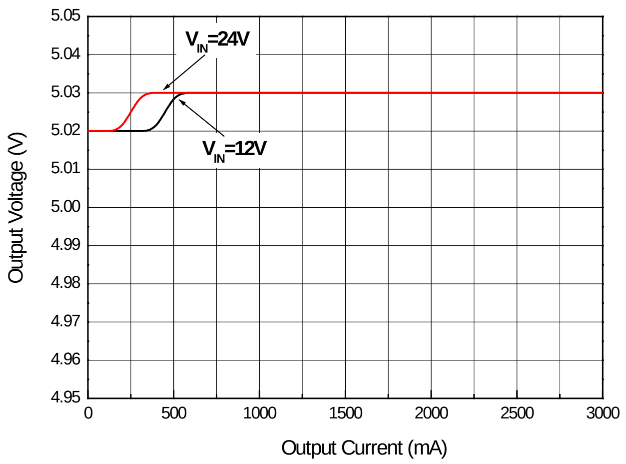


Fig. 3 Output Voltage vs. Output Current (V_{OUT}=5V)

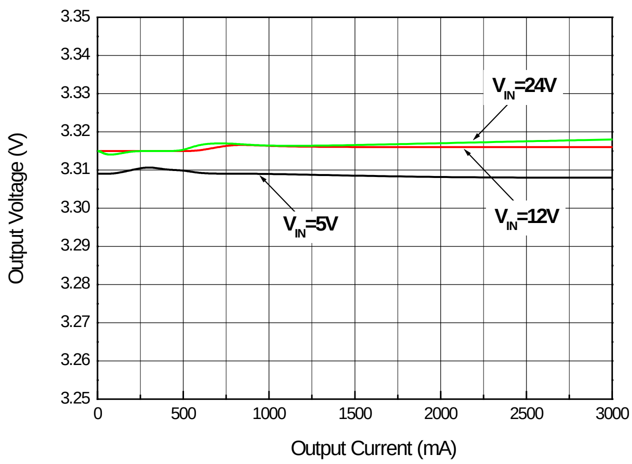


Fig. 4 Output Voltage vs. Output Current (V_{OUT}=3.3V)

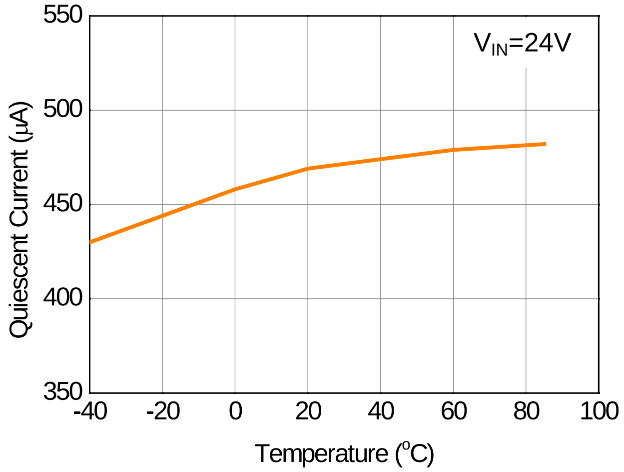


Fig. 5 Quiescent Current vs. Temperature

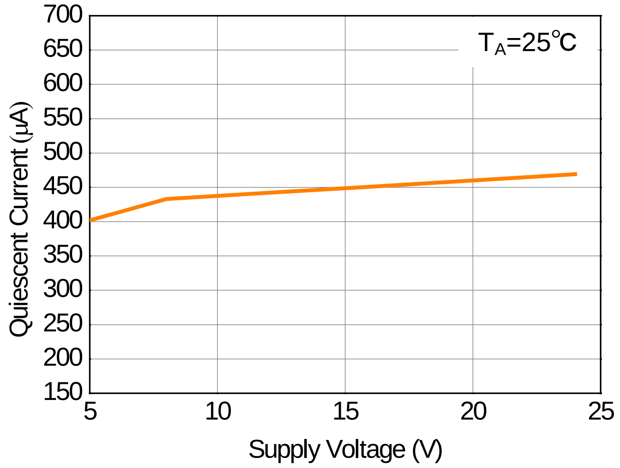


Fig. 6 Quiescent Current vs. Supply Voltage

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

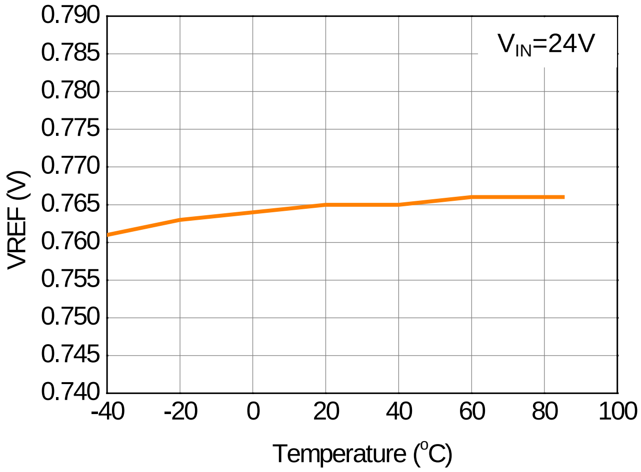


Fig. 7 V_{REF} vs. Temperature

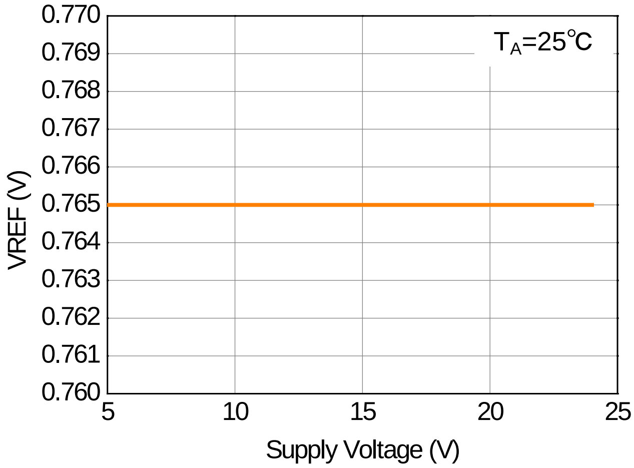


Fig. 8 V_{REF} vs. Supply Voltage

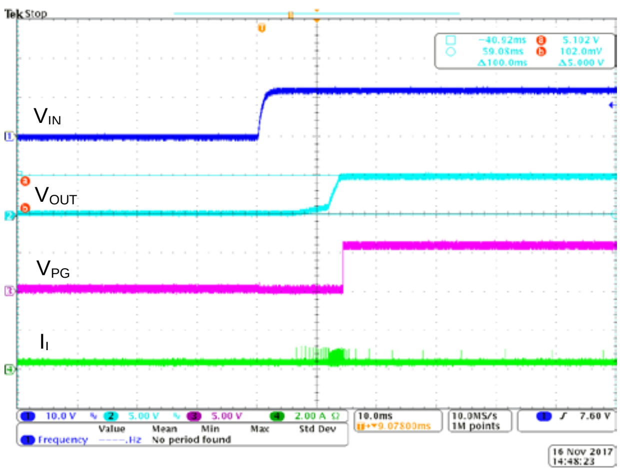


Fig. 9 V_{IN} Power On (V_{IN}=12V, V_{OUT}=5V, I_{OUT}=0A)

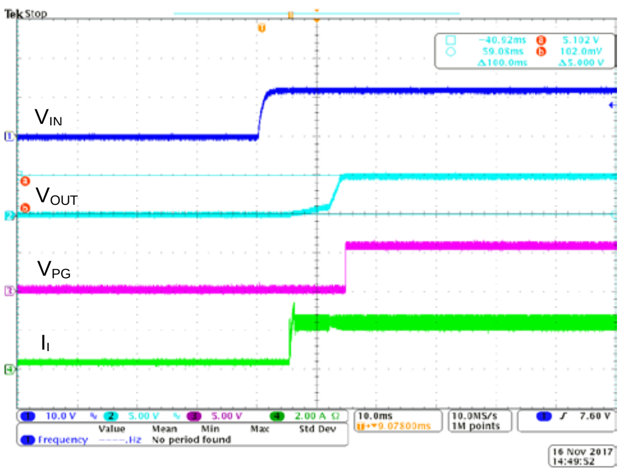


Fig. 10 V_{IN} Power On (V_{IN}=12V, V_{OUT}=5V, I_{OUT}=3A)

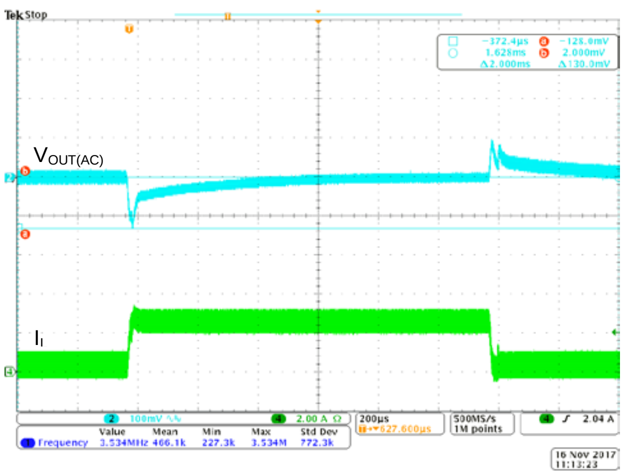


Fig. 11 Load Transient (V_{IN}=12V, V_{OUT}=3.3V, I_O=0.3-2.7A)

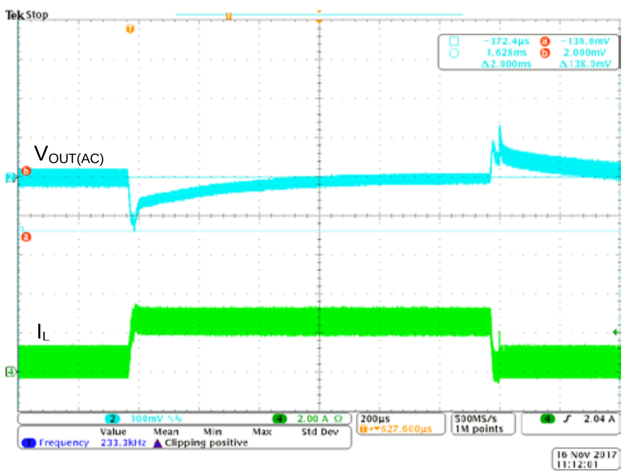


Fig. 12 Load Transient (V_{IN}=24V, V_{OUT}=3.3V, I_O=0.3-2.7A)

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

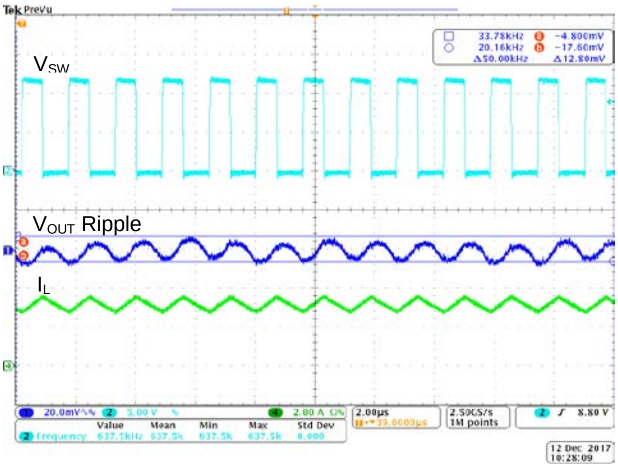
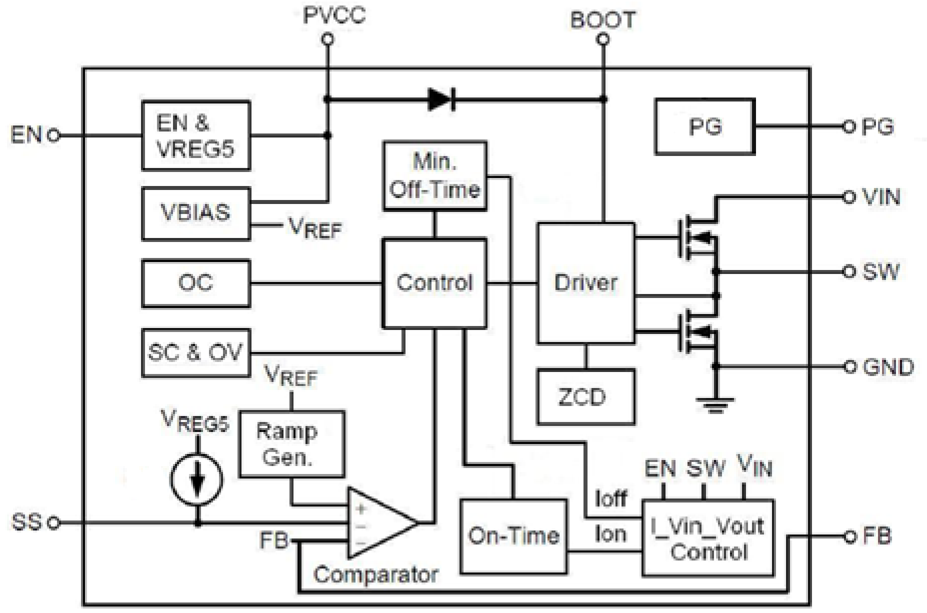


Fig. 13 Output Ripple (V_{IN}=12V, V_{OUT}=5V, I_{OUT}=3A)

BLOCK DIAGRAM



Functional Block Diagram

PIN DESCRIPTIONS

Pin No.	Pin Name	Pin Function
1	BOOT	High Side Gate Drive Boost Input. BOOT supplies the drive for the high-side N-Channel MOSFET switch. Connect a 0.1μF or greater capaitor from BOOT to SW to power the high-side switch.
2	VIN	Power Input. VIN supplies power to the IC, as well as the step-down converter switches.Bypass VIN to GND with a suitabley large capacitor to eliminate noise on the input to the IC.
3	SW	Power Switching Output. SW is the switching node that supplies power to the output. Connect the output LC filter from switch to the output load. Note that a capacitor is required from SW to BOOT to power the high-side switch.
4	PG	Power Good Indicator.
5	FB	Feedback Input. FB senses the output voltage to regulate that voltage. Drive feedback with a resistive voltage divider from the output voltage.
6	PVCC	Internal Regulator Output. Connect a capacitor to GND to stabilize output voltage.
7	EN	Enable Input. EN is a digital input that turns the regulator on or off. Drive EN high to turn on the regulator. Drive it low to turn it off.
8	SS	Soft Star Control Input. SS controls the soft star period. Connect a capacitor from SS to GND to set the soft-star period. A 3.9nF capacitor sets the soft-star period to about 1.2ms.
9	GND (Exposed Pad)	Ground.

APPLICATION INFORMATION

The AIC2893 is an adaptive on time control synchronous step down converter that can support the input voltage range from 4.5V to 30V and the output current can be up to 3A. During normal operation, the AIC2893 can regulate its output voltage through a feedback control circuit, which is composed of a comparator, a ramp generator, an on-time timer and several control signal generators. At the beginning of the switching cycle, the main power switch will be turned on and the synchronous power switch will be turned off through anti-short-through block. The main power switch will be turned off after the internal on-time timer expires. When the main power switch is turned off, the synchronous power switch will be turned on until feedback voltage signal is lower than the summing signal of reference voltage signal and ramp signal or the inductor current starts to reverse. The AIC2893 will enter discontinuous conduction mode (DCM) operation while working at light load conditions.

Shutdown

By connecting the EN pin to GND, the AIC2893 can be shut down to reduce the supply current to 0.1μA (typical). At this operation mode, the output voltage of step-down converter is equal to 0V.

Soft-Start

The AIC2893 provides the soft-start function. Initially, the voltage at SS pin is 0V. Then an internal current source charges an external soft-start capacitor. During the soft-start period, the voltage at SS pin will limit the feedback threshold voltage at FB pin. When the voltage at SS pin is higher than reference voltage, the feedback threshold voltage at FB pin reaches the desired value. A 3.9nF capacitor sets the soft-star period to about 1.2ms.

The soft-start capacitor is discharged to GND when the EN pin is connected to GND.

Power Good Indicator

AIC2893 contains an on-chip comparator for power good detection. If the output voltage is lower than power good low threshold, the output voltage of PG pin will be pulled low.

Over Current Protection

The AIC2893 has a cycle-by-cycle current limit to protect the internal power switches. The cycle-by-cycle current limit protection directly limits inductor peak current. When the current limit protection is activated continually for a period, the AIC2893 will shut down the output power stage and then initiate the soft-start sequence. If the over current condition still exists after the soft-start ends, the AIC2893 will repeat this operation mode until the over current condition is released.

Over Temperature Protection

The AIC2893 includes a thermal-limiting circuit, which is designed to protect the device from excessive temperature. When the junction temperature exceeds T_J=150ºC, the thermal-limiting circuit turns the internal power switches off and allows the IC to cool. The hysteretic of the over temperature protection is 30°C (typ.).

Inductor

The inductor selection depends on the current ripple of inductor, the input voltage and the output voltage.

$$L \geq \frac{V_{OUT}}{f_{SW} \cdot \Delta I_L}\left(1-\frac{V_{OUT}}{V_{IN}}\right)$$

Accepting a large current ripple of inductor allows the use of a smaller inductance. However, higher current ripple of inductor can cause higher output ripple voltage and large core loss. By setting an acceptable current ripple of inductor, a suitable inductance can be obtained from above equation.

In addition, it is important to ensure the inductor saturation current exceeds the peak value of inductor current in application to prevent core saturation. The peak value of inductor current can be calculated according to the following equation.

$$I_{PEAK} = I_{OUT(max)} + \frac{V_{OUT}}{2 \times f_{SW} \cdot L}\left(1 - \frac{V_{OUT}}{V_{IN}}\right)$$

Input Capacitor and Output Capacitor

To prevent the high input voltage ripple and noise resulted from high frequency switching, the use of low ESR ceramic capacitor for the maximum RMS current is recommended. The approximated RMS current of the input capacitor can be calculated according to the following equation.

$$I_{CINRMS} \approx \sqrt{I^2_{OUT(MAX)} \times \frac{V_{OUT}(V_{IN} - V_{OUT})}{V^2_{IN}} + \frac{\Delta I^2_L}{12}}$$

At least 20μF ceramic capacitor is suggested for the input capacitor.

In order to avoid the high input voltage spike to damage the device and noise coupling, a 0.1μF input bypass capacitor must be placed from VIN to GND. The 0.1μF input bypass capacitor and the input capacitor should be placed as close to the IC as possible for the best.

The selection of output capacitor depends on the required output voltage ripple. The output voltage ripple can be expressed as:

$$\Delta V_{OUT} = \frac{\Delta I_L}{8 \times f_{OSC} \cdot C_{OUT}} + ESR \cdot \Delta I_L$$

For lower output voltage ripple, the use of low ESR ceramic capacitor is recommended. The tantalum capacitor can also be used well, but its ERS is larger than that of ceramic capacitor. 44μF ceramic capacitor will be needed for the output capacitor.

When choosing the input and output ceramic capacitors, X5R and X7R types are recommended because they retain their capacitance over wider ranges of voltage and temperature than other types.

Setting the Output Voltage

The output voltage is set using a resistive voltage divider connected from the output voltage to FB. The voltage divider divides the output voltage down to the feedback voltage by the ratio:

$$V_{FB} = V_{OUT}\frac{R_2}{R_1 + R_2}$$

Thus the output voltage is:

$$V_{OUT} = V_{REF} \times \frac{R_1 + R_2}{R_2}$$

For example, for a 3.3V output voltage and V_{REF}=0.765V, R_2 is 22kΩ, and R_1 is 72.9kΩ.

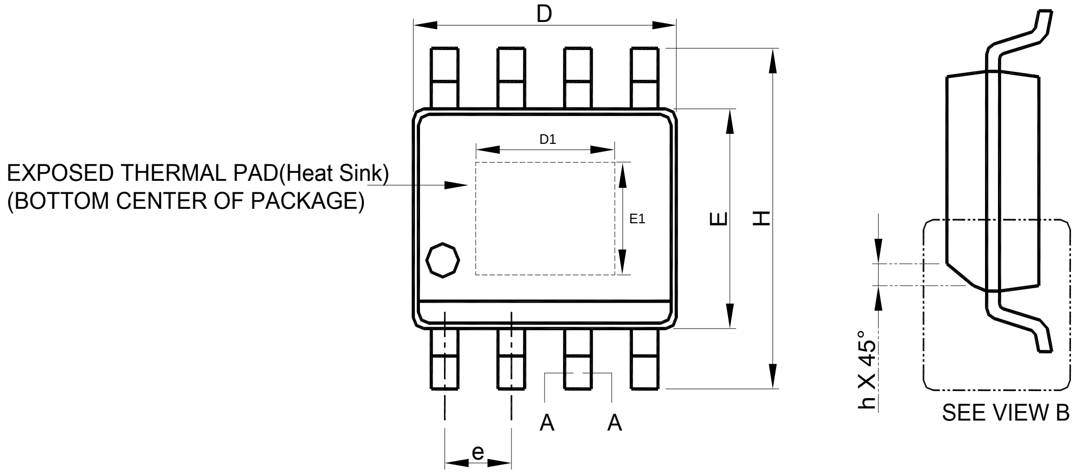
Layout Consideration

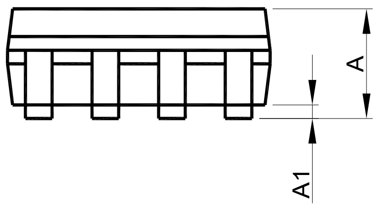
In order to ensure a proper operation of AIC2893, the following points should be managed comprehensively.

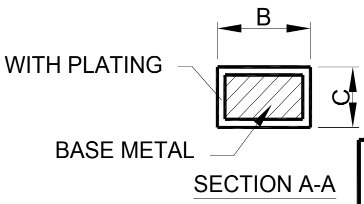
1. The 0.1μF input bypass capacitor, input capacitor and V_{IN} should be placed as close as possible to each other to reduce the input voltage ripple and noise.
2. The output loop, which is consisted of the inductor, the internal power switches and the output capacitor, should be kept as small as possible.
3. The routes with large current should be kept short and wide.
4. Logically the large current on the converter should flow at the same direction.
5. In order to prevent the effect from noise, the IC's GND pin should be placed close to the ground of the input bypass capacitor.
6. The FB pin should be connected to the feedback resistors directly and the route should be away from the noise sources.

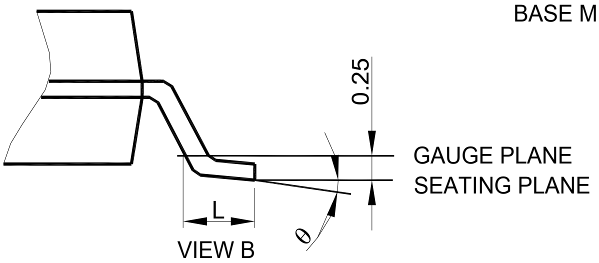
PHYSICAL DIMENSIONS

- **SOP-8 Exposed Pad**









Note : 1. Refer to JEDEC MS-012E.
2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side .
3. Dimension "E" does not include inter-lead flash or protrusions.
4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

SYMBOL	SOP-8 Exposed Pad	
	MILLIMETERS	
	MIN.	MAX.
A	1.35	1.75
A1	0.00	0.15
B	0.31	0.51
C	0.17	0.25
D	4.80	5.00
D1	1.50	3.50
E	3.80	4.00
E1	1.0	2.55
e	1.27 BSC	
H	5.80	6.20
h	0.25	0.50
L	0.40	1.27
θ	0°	8°

Note: