

600kHz, 18V, 3A Synchronous Step-Down Converter In SOT563

FEATURES

- High Efficiency: Up to 93%(@3.3V)
- 600kHz Frequency Operation
- Up to 3.0A Output Current
- Low $R_{DS(ON)}$ for internal switches: 70m Ω /35m Ω (top/bottom)
- 4.5V to 18V Input Voltage Range
- 0.807V Reference
- Current mode AOT control to achieve fast transient responses
- Integrated internal compensation
- Stable with Low ESR Ceramic Output Capacitors
- Over Current Protection with Hiccup Mode
- Thermal Shutdown
- Inrush Current Limit and Soft Start
- Available in SOT563-6 Package

DESCRIPTION

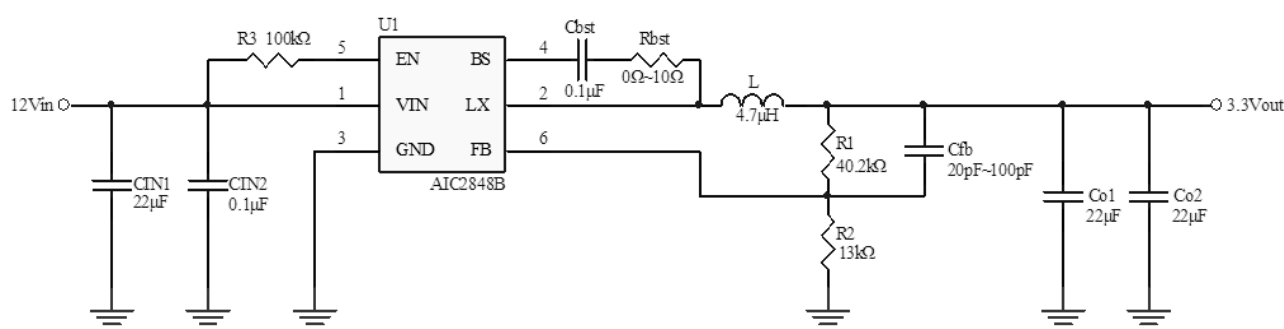
The AIC2848B is a high efficiency 600kHz current mode adaptive on-time (CM-AOT) control mode synchronous step-down DC-DC converter capable of delivering 3A current. AIC2848B integrates main switch and synchronous switch with very low $R_{DS(ON)}$ to minimize the conduction loss. Low output voltage ripple and small external inductor and capacitor size are achieved with 600kHz switching frequency. It adopts the CM-AOT architecture to achieve fast transient responses for high voltage step down applications.

The AIC2848B requires a minimum number of readily available standard external components and is available in a space saving 6-pin SOT563-6 ROHS compliant package.

APPLICATIONS

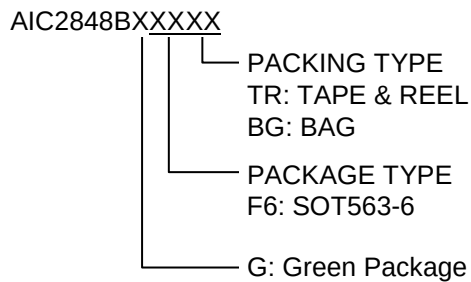
- Digital Set Top Boxes
- Flat Panel Television and Monitors
- Notebook Computer
- Wireless and DSL Modems

APPLICATIONS CIRCUIT



AIC2848B Typical Application Circuit

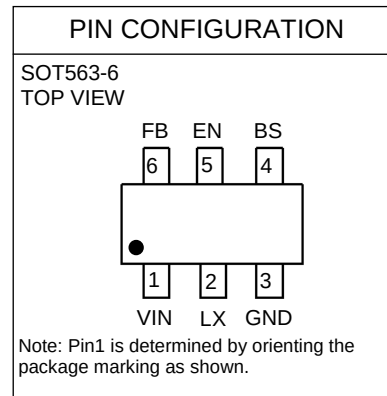
■ ORDERING INFORMATION



Example: AIC2848BGF6TR
 → SOT563-6 Green Package and Tape
 & Reel Packing Type

Top Mark: TH3XXX (TH3: Device Code, XXX: Inside Code)

Part No.	Marking
AIC2848BGF6	TH3XXX



■ ABSOLUTE MAXIMUM RATINGS

VIN Pin and EN Pin Voltage	- 0.3V to 20V
LX Pin Voltage	- 0.3V to 20V
FB Pin Voltage	- 0.3V to 6V
BS Pin Voltage	0.3V to 23V
Junction Temperature (Note2).....	160°C
Lead Temperature(Soldering, 10 sec).....	260°C
Storage Temperature Range	- 65°C ~ 150°C
Operating Junction Temperature Range	- 40°C ~ 125°C
Thermal Resistance Junction to Ambient, θ_{JA} (Note3)	SOT563-6..... 130°C/W
Power Dissipation	SOT563-6..... 600mW

(Assume no Ambient Airflow, no Heatsink)

Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

■ ELECTRICAL CHARACTERISTICS

($V_{IN}=12V$, $V_{OUT}=3.3V$, $T_A=25^{\circ}C$, unless otherwise specified.) (Note1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range	V_{IN}		4.5		18	V
OVP Threshold	V_{OVP}			19.3		V
UVLO Rising Threshold	V_{UVLO}		4.0			V
UVLO Hysteresis				0.45		V
Quiescent Current		$V_{EN}=2.0V$, $I_{OUT}=0A$, $V_{FB}=V_{REF} \times 105\%$		300		μA
Shutdown Current		$V_{IN}=12V$, $V_{EN}=0V$		5	10	μA
Regulated Feedback Voltage	V_{FB}	$T_A=25^{\circ}C$, $4.5V \leq V_{IN} \leq 18V$	0.791	0.807	0.823	V
High-Side Switch On-Resistance	$R_{DS(ON)1}$			70		m Ω
Low-Side Switch On-Resistance	$R_{DS(ON)2}$			35		m Ω
High-Side Switch Leakage Current		$V_{EN}=0V$, $V_{LX}=0V$	1		10	μA
Switch Vally Current Limit		Minimum Duty Cycle		4		A
On Time		$V_{IN}=12V$, $V_{OUT}=1.2V$, $I_{OUT}=1A$	132	166	200	ns
Oscillation Frequency	f_{OSC}			600		kHz
Maximum Duty Cycle	D_{MAX}	$V_{FB}=0.807V$		65		%
Minimum On-Time	T_{ON}			80		ns
Soft Start Time				1		ms
Hiccup on Time				1.4		ms
Hiccup Time Before Restart				4		ms
EN Input High Level Voltage			1.2			V
EN Input Low Level Voltage					0.4	V
Thermal Shutdown Threshold (Note4)	T_{SD}			165		$^{\circ}C$
Thermal Shutdown Hysteresis (Note4)				30		$^{\circ}C$

Note 1: Specifications are production tested at $T_A=25^{\circ}\text{C}$. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with Statistical Quality Controls (SQC).

Note 2: T_J is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula: $T_J = T_A + (P_D \times \theta_{JA})$. The maximum allowable continuous power dissipation at any ambient temperature is calculated by $P_{D(\text{MAX})} = (T_{J(\text{MAX})} - T_A) / \theta_{JA}$.

Note 3: Measured on JESD51-7, 4-layer PCB.

Note 4: Thermal shutdown threshold and hysteresis are guaranteed by design.

■ TYPICAL PERFORMANCE CHARACTERISTICS

$V_{OUT}=3.3V$, $L=4.7\mu H$, $DCR=30m\Omega$

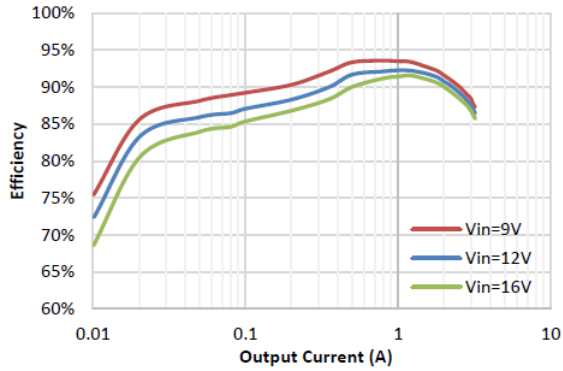


Fig. 1 Efficiency vs. Output Current ($V_{OUT}=3.3V$)

$V_{OUT}=5V$, $L=4.7\mu H$, $DCR=30m\Omega$

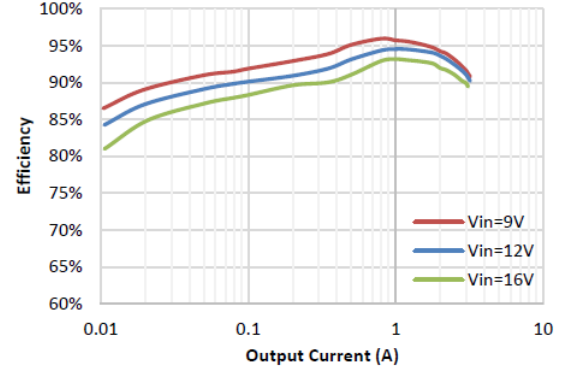


Fig. 2 Efficiency vs. Output Current ($V_{OUT}=5V$)

$V_{OUT}=1.2V$, $L=2.2\mu H$, $DCR=20m\Omega$

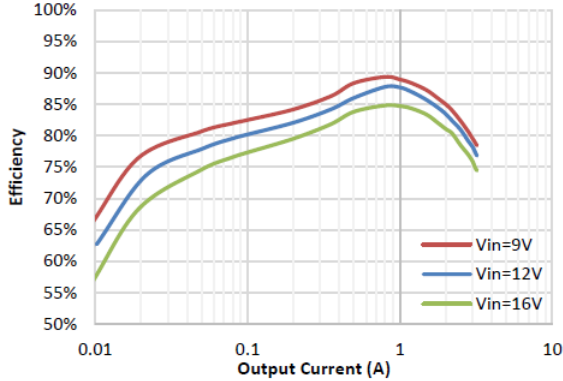


Fig. 3 Efficiency vs. Output Current ($V_{OUT}=1.2V$)

$V_{IN}=12V$, $T_A=25^\circ C$

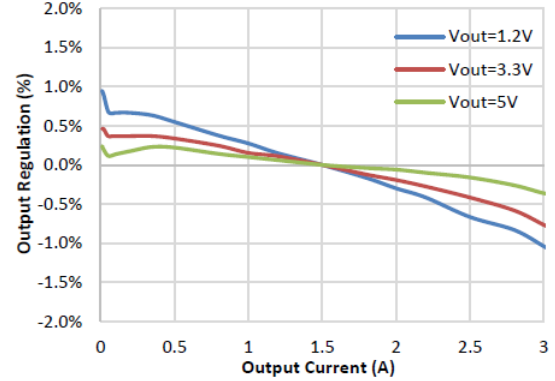


Fig. 4 Load Regulation ($V_{IN}=12V$)

$V_{IN}=12V$, $V_{OUT}=3.3V$, No Load

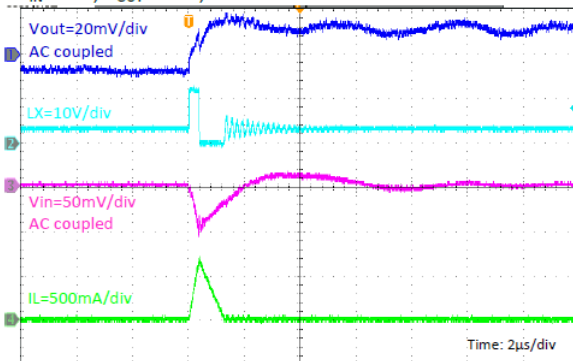


Fig. 5 Steady State Operation

$V_{IN}=12V$, $V_{OUT}=3.3V$, $I_O=3A$

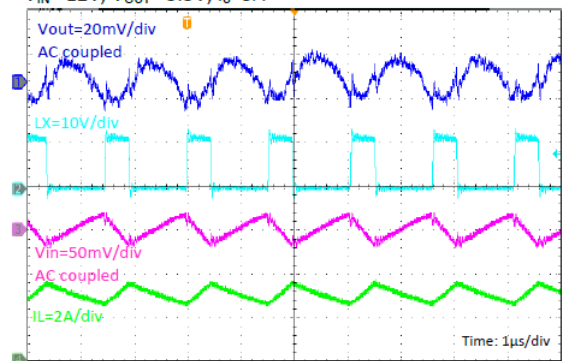


Fig. 6 Steady State Operation

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

$V_{IN}=12V$, $V_{OUT}=3.3V$, No Load

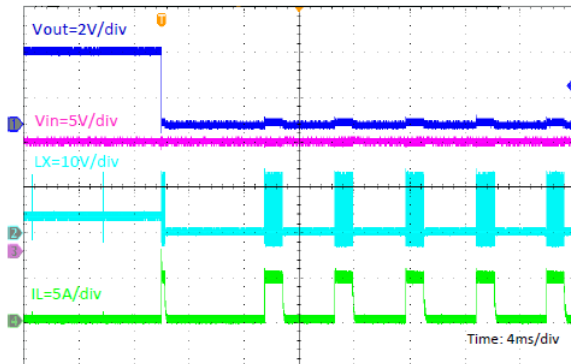


Fig. 7 Output Short Entry

$V_{IN}=12V$, $V_{OUT}=3.3V$, No Load

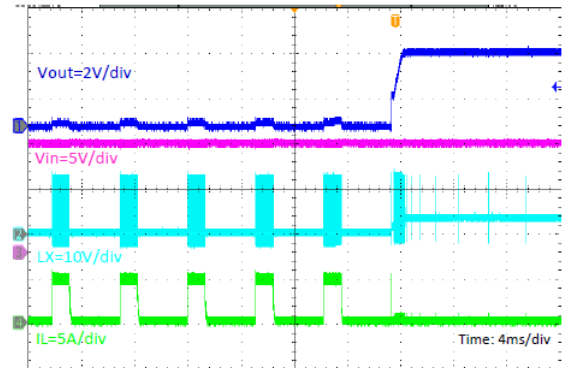


Fig. 8 Output Short Recovery

$V_{IN}=12V$, $V_{OUT}=3.3V$, $I_o=3A$

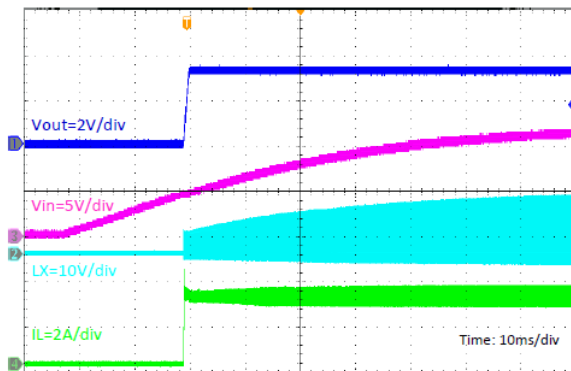


Fig. 9 Input Power On

$V_{IN}=12V$, $V_{OUT}=3.3V$, $I_o=3A$

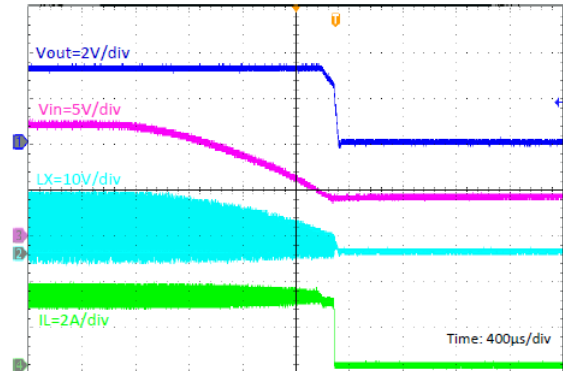


Fig. 10 Input Power Down

$V_{IN}=12V$, $V_{OUT}=3.3V$, $I_o=3A$

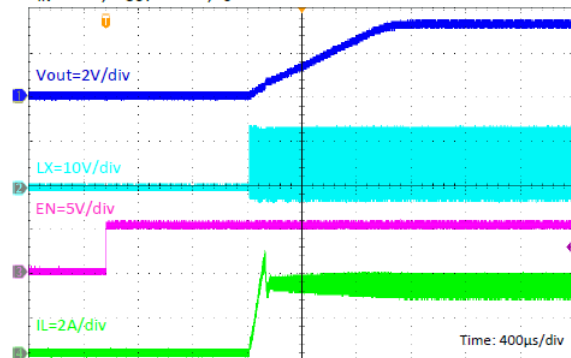


Fig. 11 EN Enable Power On

$V_{IN}=12V$, $V_{OUT}=3.3V$, $I_o=3A$

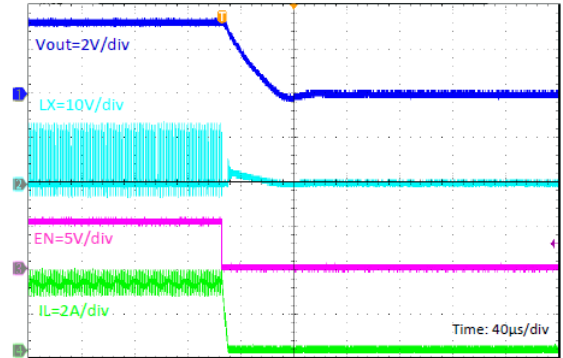


Fig. 12 EN Disable Power Down

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

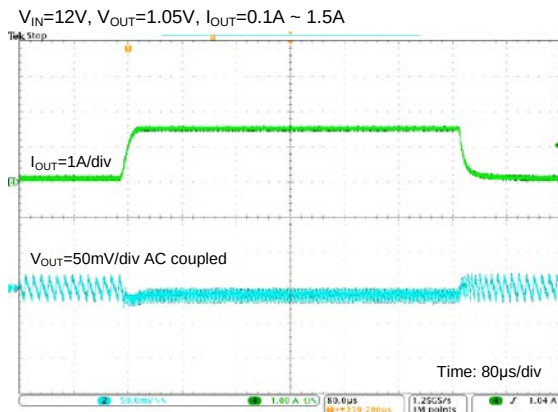


Fig. 13 Load Transient

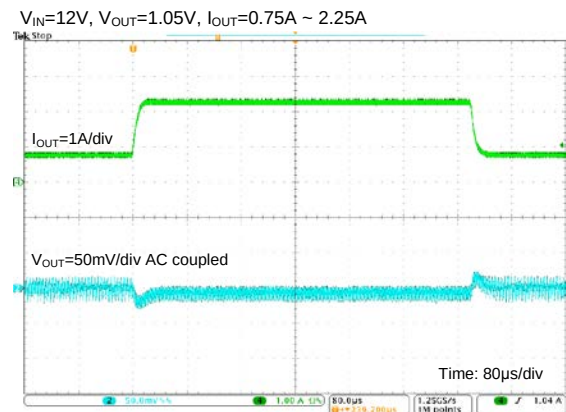


Fig. 14 Load Transient

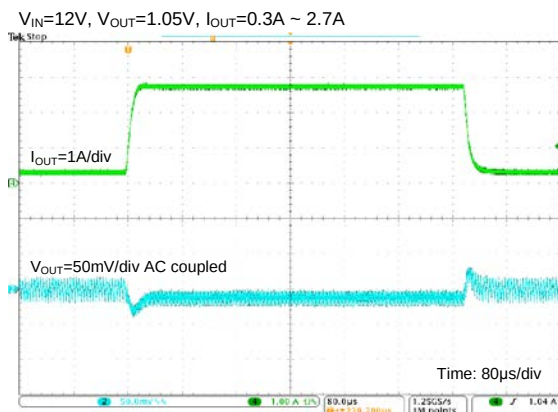


Fig. 15 Load Transient

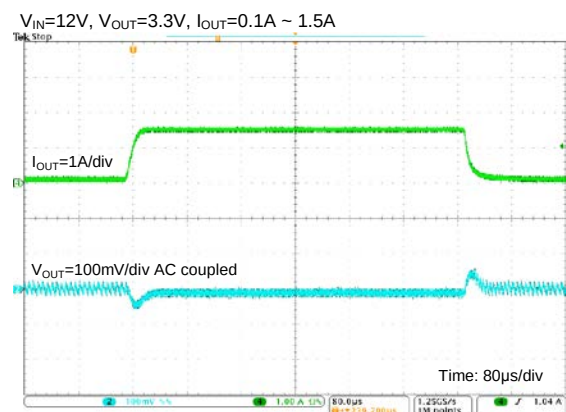


Fig. 16 Load Transient

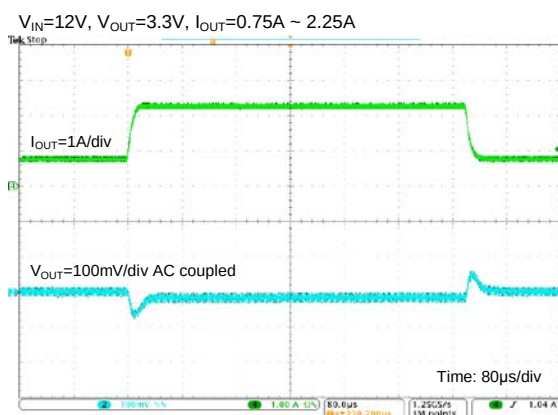


Fig. 17 Load Transient

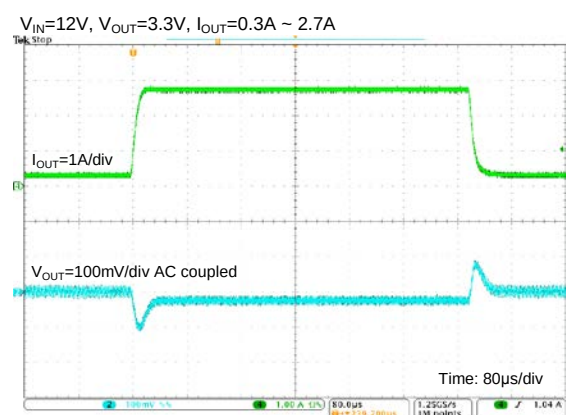


Fig. 18 Load Transient

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

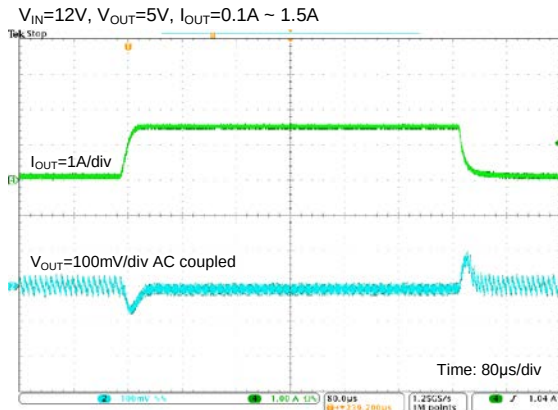


Fig. 19 Load Transient

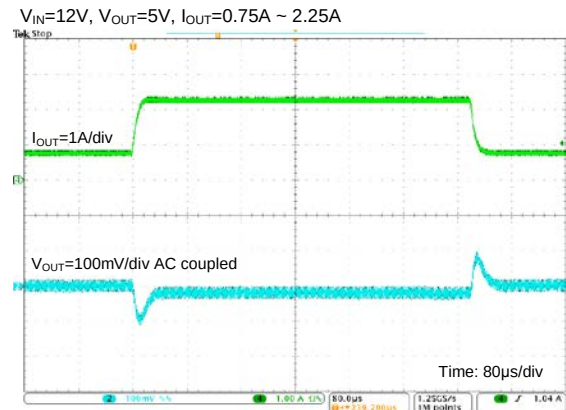


Fig. 20 Load Transient

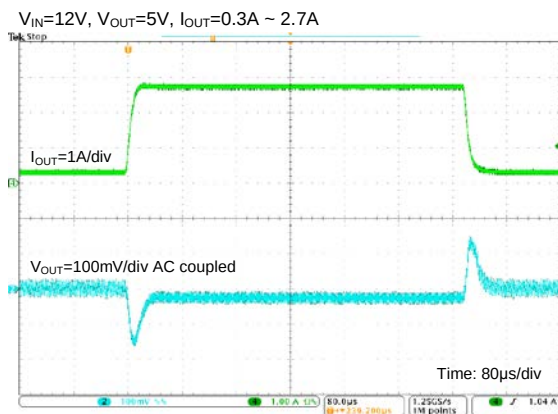
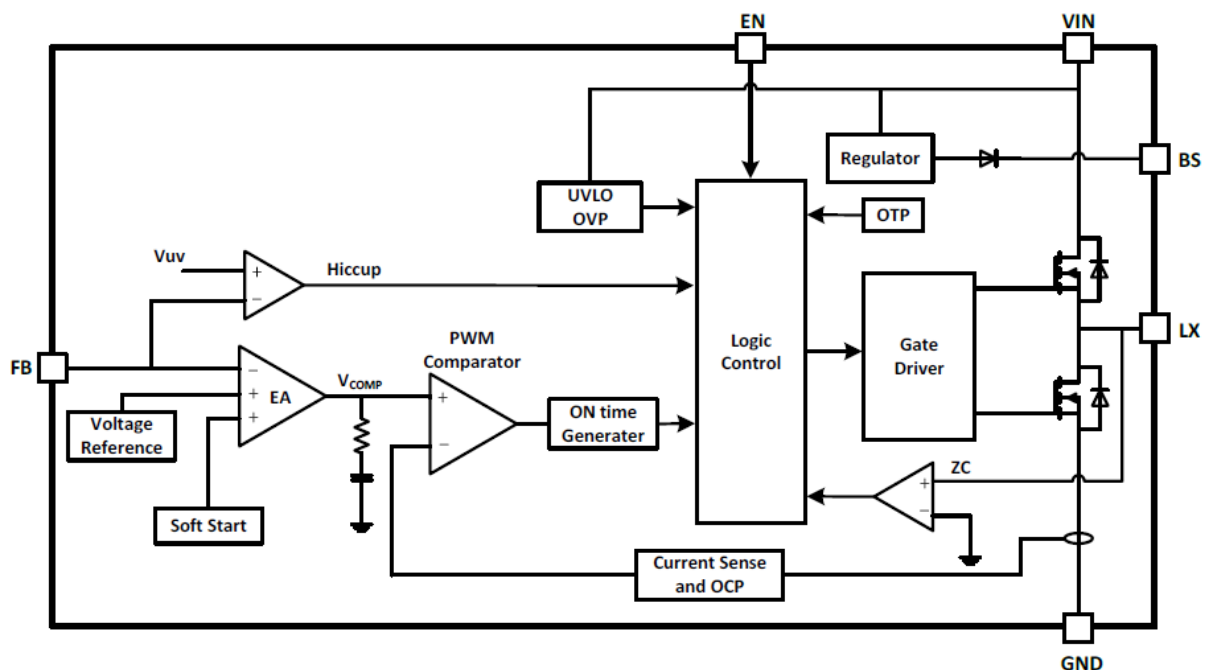


Fig. 21 Load Transient

■ BLOCK DIAGRAM



Functional Block Diagram of AIC2848B

■ PIN DESCRIPTIONS

Pin No.	Pin Name	Pin Function
1	VIN	Power supply Pin.
2	LX	Switching Pin.
3	GND	Ground.
4	BS	Bootstrap. A capacitor connected between LX and BS pins is required to form a floating supply across the high-side switch driver.
5	EN	Drive this pin to a logic-high to enable the IC. Drive to a logic-low to disable the IC and enter micro-power shutdown mode. Do not leave EN pin floating.
6	FB	Output voltage feedback input. Connect FB to the center point of the external resistor divider.

■ APPLICATION INFORMATION

Internal Regulator

The AIC2848B is a current mode adaptive on-time (CM-AOT) step down DC/DC converter that provides excellent transient response with no extra external compensation components. This device contains low resistance, high voltage high side and low side power MOSFETs, and operates at 600kHz operating frequency to ensure a compact, high efficiency design with excellent AC and DC performance.

Error Amplifier

AIC2848B adopts operational transconductance amplifier (OTA) as error amplifier. The error amplifier compares the FB pin voltage with the internal FB reference (V_{REF}) and outputs a current proportional to the difference between the two. This output current is then used to charge or discharge the internal compensation network to form the V_{COMP} voltage, which is used to compare with the low side power MOSFET current sensing signal and trigger on time pulse. The optimized internal compensation network minimizes the external component counts and simplifies the control loop design.

Internal Soft-Start

The soft-start is implemented to prevent the converter output voltage from overshooting during startup. When the chip starts, the internal circuitry generates a soft-start voltage (SS) ramping up from 0V to 0.807V. When it is lower than the internal FB reference (V_{REF}), SS overrides REF so the error amplifier uses SS as the reference. When SS is higher than V_{REF} , V_{REF} regains control. The SS time is internally fixed to 1ms typically.

Over-Current-Protection and Short Circuits

Protection

The AIC2848B has cycle-by-cycle valley current limit function. When the inductor current valley value is

larger than the valley current limit during low side MOSFET on state, the device enters into valley over current protection mode and low side MOSFET keeps on state until inductor current drops down to the value equal or lower than the valley current limit, and then on time pulse could be generated and high side MOSFET could turn on again.

If the output is short to GND and the output voltage drop until feedback voltage V_{FB} is below the output under-voltage V_{UV} threshold which is typically 45% of V_{REF} , AIC2848B enters into hiccup mode to periodically disable and restart switching operation. The hiccup mode helps to reduce power dissipation and thermal rise during output short condition. The period of AIC2848B hiccup mode is typically 5.4ms.

Startup and Shutdown

If both VIN and EN are higher than their appropriate thresholds, the chip starts switching operation. The reference block starts first, generating stable reference voltage and currents, and then the internal regulator is enabled. The regulator provides stable supply for the remaining circuitries. Three events can shut down the chip: EN low, VIN low and thermal shutdown. In the shutdown procedure, the signaling path is first blocked to avoid any fault triggering. The V_{COMP} voltage and the internal supply rail are then pulled down. The floating driver is not subject to this shutdown command.

Setting the Output Voltage

The external resistor divider is used to set the output voltage (see Typical Application on page 1). Choose R1 to be around 40kΩ for optimal transient response. R2 is then given by:

$$R_2 = \frac{R_1}{\frac{V_{out}}{V_{FB}} - 1}$$

Table 1 Selection for Common Output Voltages

V _{OUT} (V)	R1(kΩ)	R2(kΩ)	C _{FB} (pF)	L(μH)
5	40.2	7.68	33	4.7
3.3	40.2	13	33	4.7
2.5	40.2	19.1	33	3.3
1.8	40.2	32.4	33	2.2
1.5	40.2	45.3	33	2.2
1.2	40.2	82	33	1.5
1	20.5	84.5	33	1.0

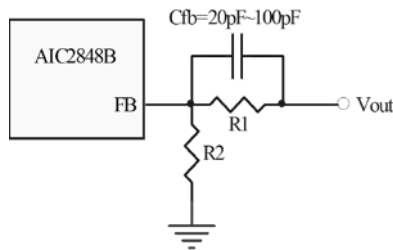


Fig. 22 Feedback Network

Selecting the Inductor

A DC current rating of at least 25% percent higher than the maximum load current is recommended for most applications. Inductance value is related to inductor ripple current value, input voltage, output voltage setting and switching frequency. The inductor value can be derived from the following equation:

$$L = \frac{V_{out} \times (V_{in} - V_{out})}{V_{in} \times \Delta I_L \times f_{OSC}}$$

Where ΔI_L is inductor ripple current. Large value inductors result in lower ripple current and small value inductors result in high ripple current, so inductor value has effect on output voltage ripple value. DC resistance of inductor which has impact on efficiency of DC/DC converter should be taken into account when selecting the inductor. The maximum inductor peak current is:

$$I_{L(MAX)} = I_{LOAD} + \frac{\Delta I_L}{2}$$

Under light load conditions, larger inductance is recommended for improved light load efficiency.

Selecting the Output Capacitor

The output capacitor (Co1 and Co2) is required to maintain the DC output voltage. Ceramic, tantalum, or low ESR electrolytic capacitors are recommended. Low ESR capacitors are preferred to keep the output voltage ripple low. The output voltage ripple can be estimated by:

$$\Delta V_{OUT} = \frac{V_{OUT}}{f_s \times L} \times \left[1 - \frac{V_{OUT}}{V_{IN}} \right] \times \left[R_{ESR} + \frac{1}{8 \times f_s \times (C_{O1} + C_{O2})} \right]$$

Where L is the inductor value and R_{ESR} is the equivalent series resistance (ESR) value of the output capacitor. In the case of ceramic capacitors, the impedance at the switching frequency is dominated by the capacitance. The output voltage ripple is mainly caused by the capacitance. For simplification, the output voltage ripple can be estimated by:

$$\Delta V_{OUT} = \frac{V_{OUT}}{8 \times f_s^2 \times L \times (C_{O1} + C_{O2})} \times \left[1 - \frac{V_{OUT}}{V_{IN}} \right]$$

In the case of tantalum or electrolytic capacitors, the ESR dominates the impedance at the switching frequency. For simplification, the output ripple can be approximated to:

$$\Delta V_{OUT} = \frac{V_{OUT}}{f_s \times L} \times \left[1 - \frac{V_{OUT}}{V_{IN}} \right] \times R_{ESR}$$

The characteristics of the output capacitor also affect the stability of the regulation system. The AIC2848B can be optimized for a wide range of capacitance and ESR values.

PCB Layout Guide

PCB layout is very important to achieve stable operation. It is highly recommended to duplicate EVB layout for optimum performance. If change is necessary, please follow these guidelines and take figures 16~17 for reference.

(1) Keep the path of switching current short and

minimize the loop area formed by Input capacitor, VIN pin and GND.

(2) Bypass ceramic capacitors are suggested to be put close to the VIN Pin.

(3) Ensure all feedback connections are short and direct. Place the feedback resistors as close to the chip as possible.

(4) VOUT, LX away from sensitive analog areas such as FB.

(5) Connect VIN, LX, and especially GND respectively to a large copper area to cool the chip to improve thermal performance and long-term reliability.

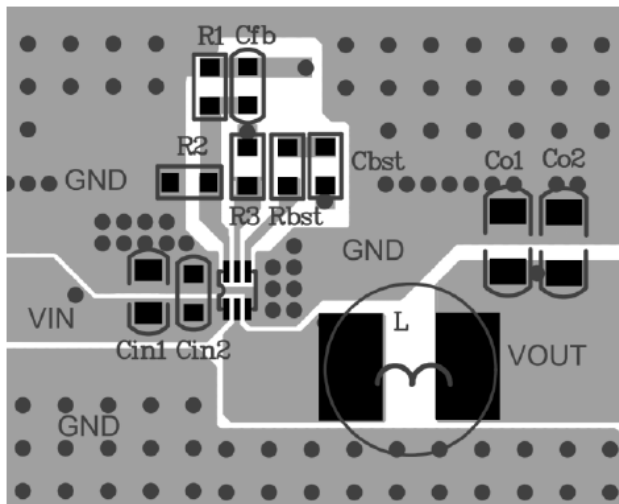


Fig. 23 Top Layer

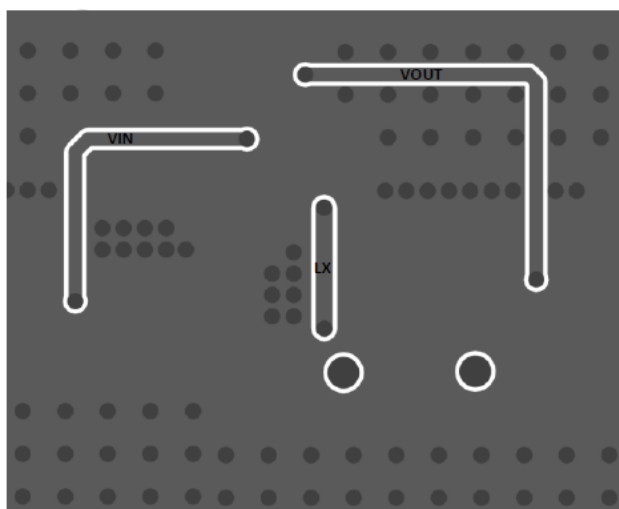


Fig. 24 Bottom Layer

APPLICATION EXAMPLES

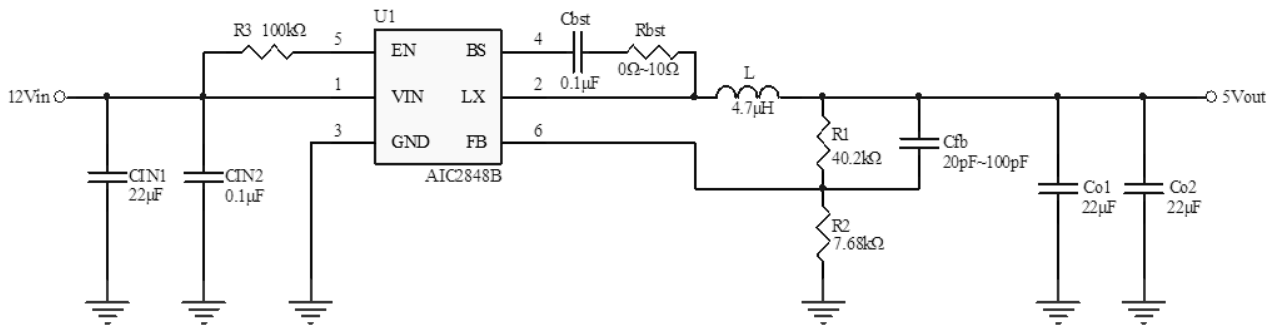


Fig. 25 AIC2848B Application Circuit for 12V_{IN}, 5V/3A Output Application

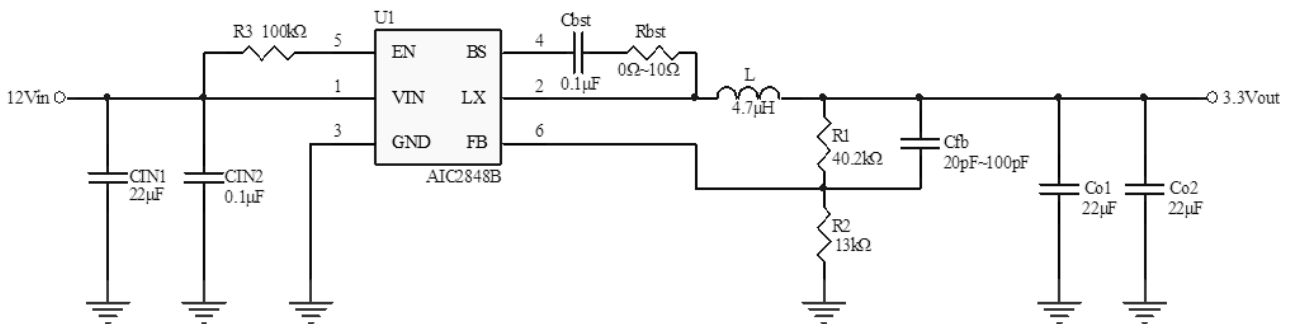


Fig. 26 AIC2848B Application Circuit for 12V_{IN}, 3.3V/3A Output Application

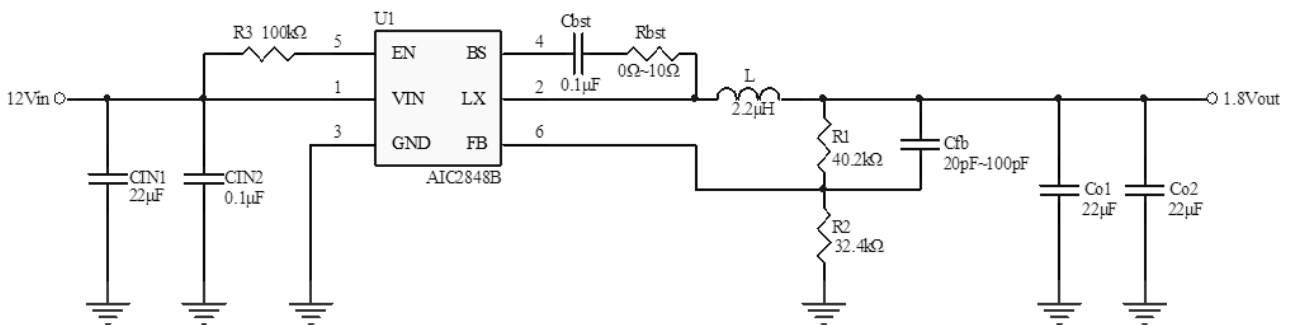


Fig. 27 AIC2848B Application Circuit for 12V_{IN}, 1.8V/3A Output Application

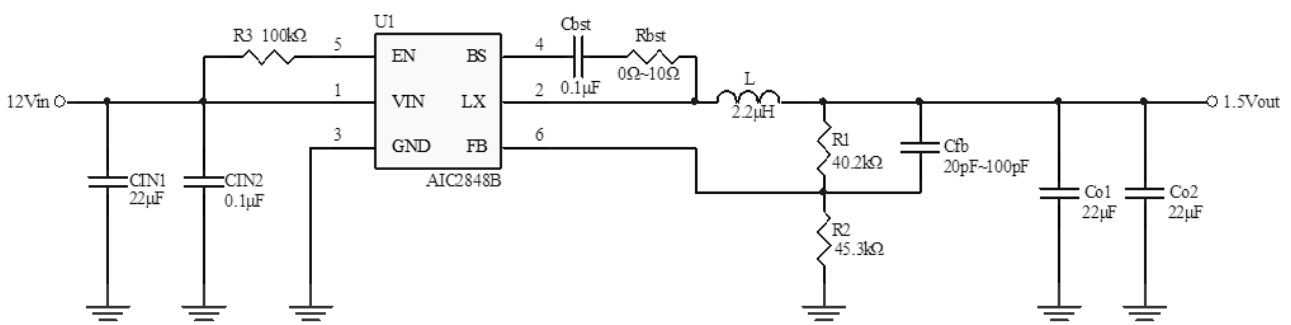


Fig. 28 AIC2848B Application Circuit for 12V_{IN}, 1.5V/3A Output Application

■ APPLICATION EXAMPLES (Continued)

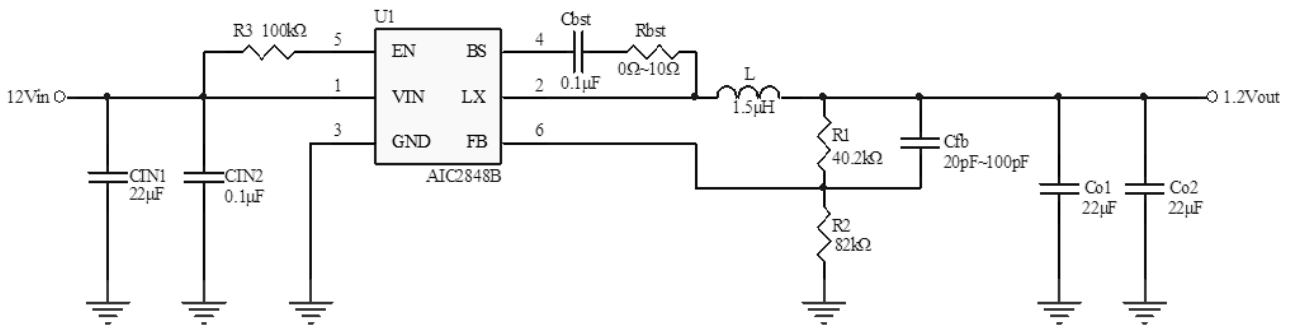


Fig. 29 AIC2848B Application Circuit for 12V_{IN}, 1.2V/3A Output Application

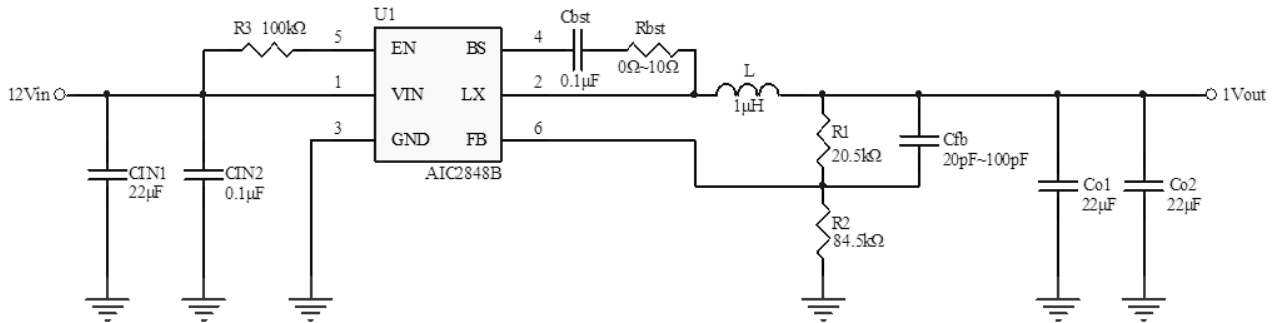
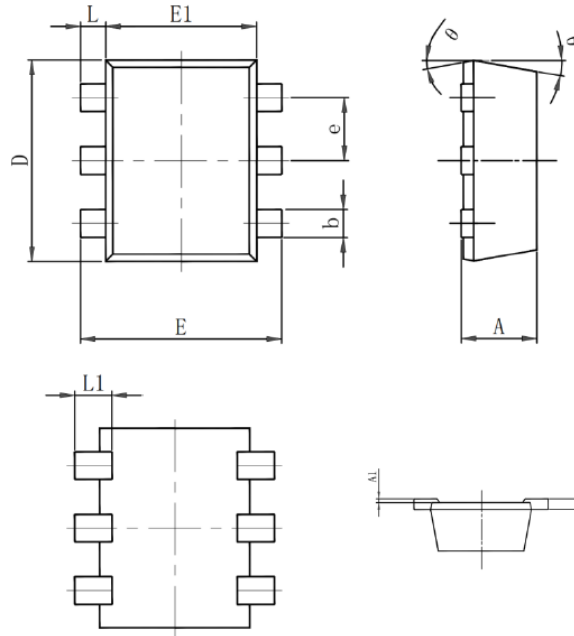


Fig. 30 AIC2848B Application Circuit for 12V_{IN}, 1V/3A Output Application

■ PHYSICAL DIMENSIONS

● SOT563-6



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.525	0.600	0.021	0.024
A1	0.000	0.050	0.000	0.002
e	0.450	0.550	0.018	0.022
c	0.090	0.180	0.004	0.007
D	1.500	1.700	0.059	0.067
b	0.170	0.270	0.007	0.011
E1	1.100	1.300	0.043	0.051
E	1.500	1.700	0.059	0.067
L	0.100	0.300	0.004	0.012
L1	0.200	0.400	0.008	0.016
θ	9° REF.		9° REF.	

Note:

- 1) All dimensions are in millimeters.
- 2) Package length does not include mold flash, protrusion or gate burr.
- 3) Package width does not include interlead flash or protrusion.
- 4) Lead popularity (bottom of leads after forming) shall be 0.10 millimeters max.
- 5) Pin 1 is lower left pin when reading top mark from left to right.

Note:

Information provided by AIC is believed to be accurate and reliable. However, we cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in an AIC product; nor for any infringement of patents or other rights of third parties that may result from its use. We reserve the right to change the circuitry and specifications without notice.

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